

0203430110300

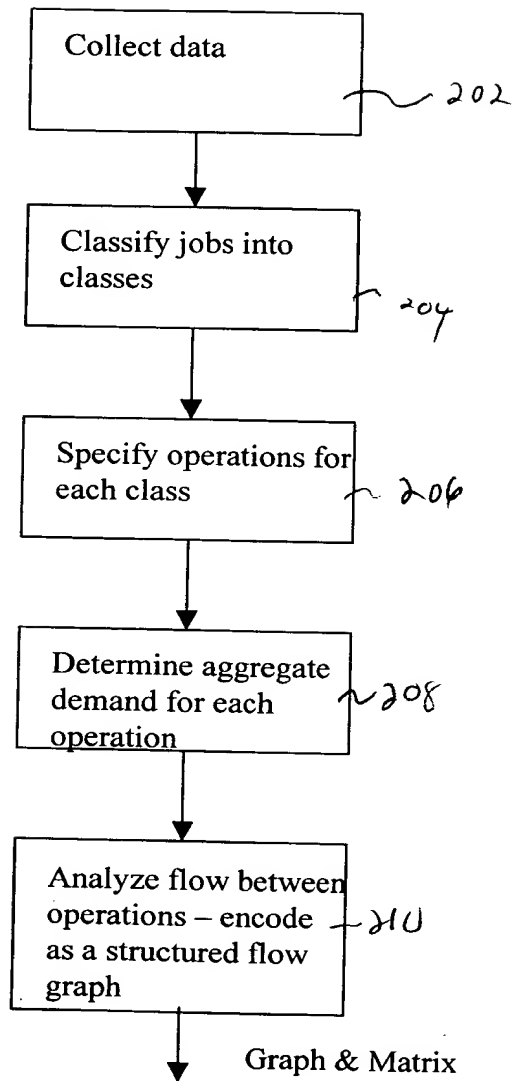


Figure 2

0906430 110 110300

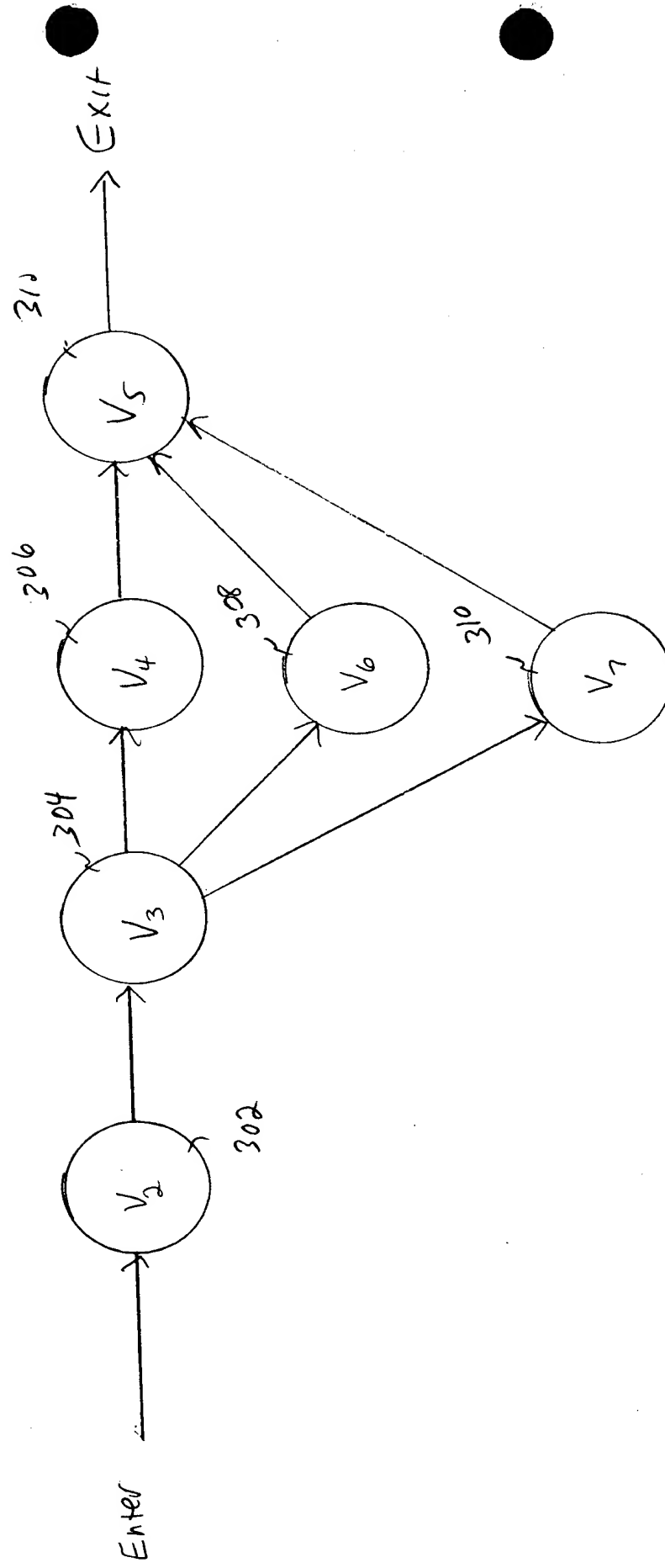


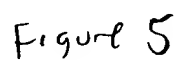
Figure 3

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graph TD; 400[Partition job flow graph into cells ~400] --> 402[Map onto physical layout ~402]; 402 --> 404[Evaluate by simulation ~404]; 404 --> 406{Intercell flows small & constraints satisfied? ~406}; 406 -- Yes --> Output["-Cell layout<br/>-Job class mapping algorithm"]; 406 -- No --> 408[Revise intercell flow metric to capture constraints in a different way ~408]; 408 --> 400;
```

The flowchart illustrates an iterative process for job class mapping. It begins with a rectangular box labeled "Partition job flow graph into cells" with a handwritten reference number "~400". An arrow points down to another rectangular box labeled "Map onto physical layout" with a handwritten reference number "~402". From there, an arrow points down to a third rectangular box labeled "Evaluate by simulation" with a handwritten reference number "~404". Below this is a diamond-shaped decision box labeled "Intercell flows small & constraints satisfied?" with a handwritten reference number "~406". If the answer is "Yes", an arrow points down to the final output: "-Cell layout" and "-Job class mapping algorithm". If the answer is "No", an arrow points left to a rectangular box labeled "Revise intercell flow metric to capture constraints in a different way" with a handwritten reference number "~408". From this box, an arrow points up and then right, looping back to the "Partition job flow graph into cells" box.

Figure 4

500



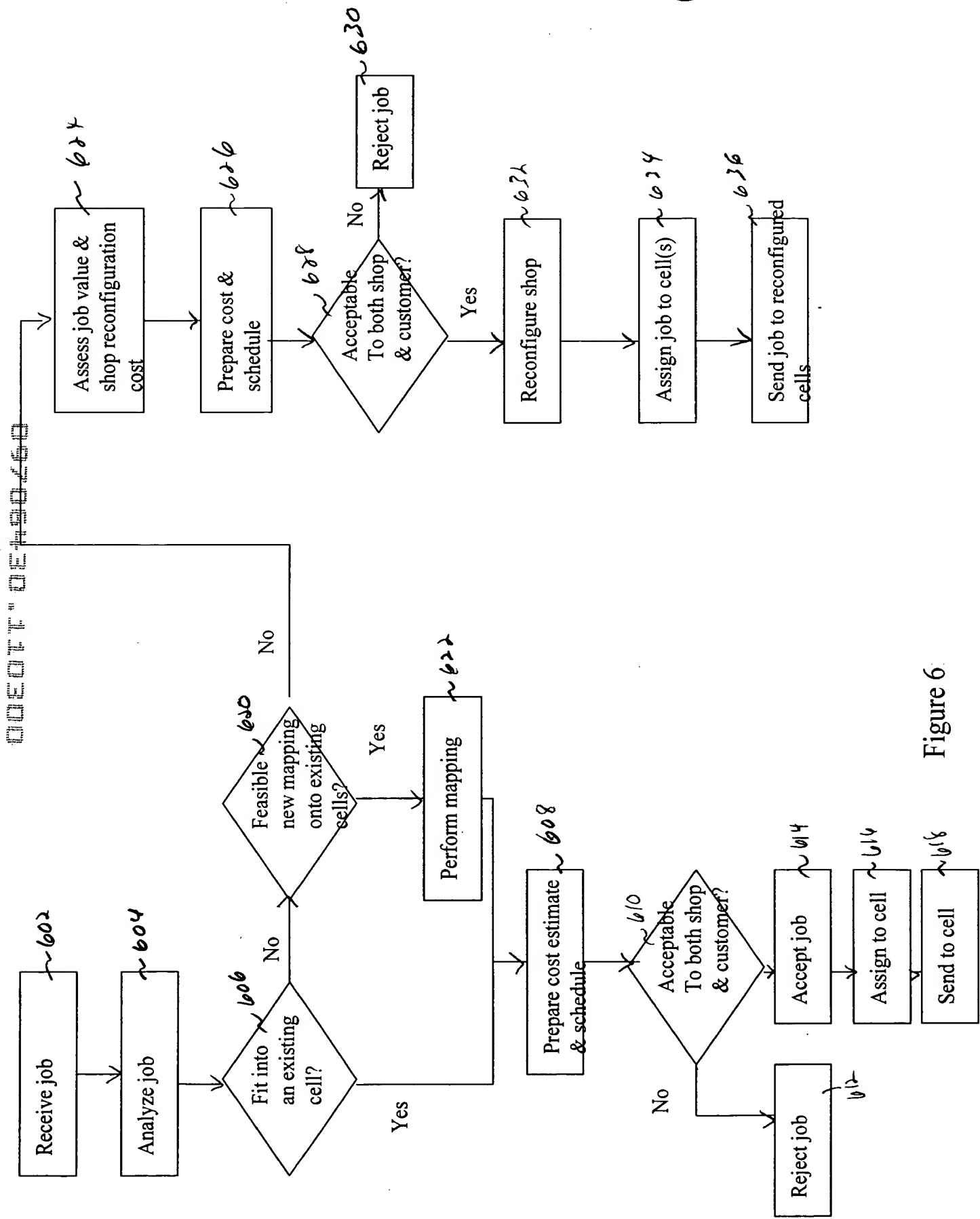


Figure 6

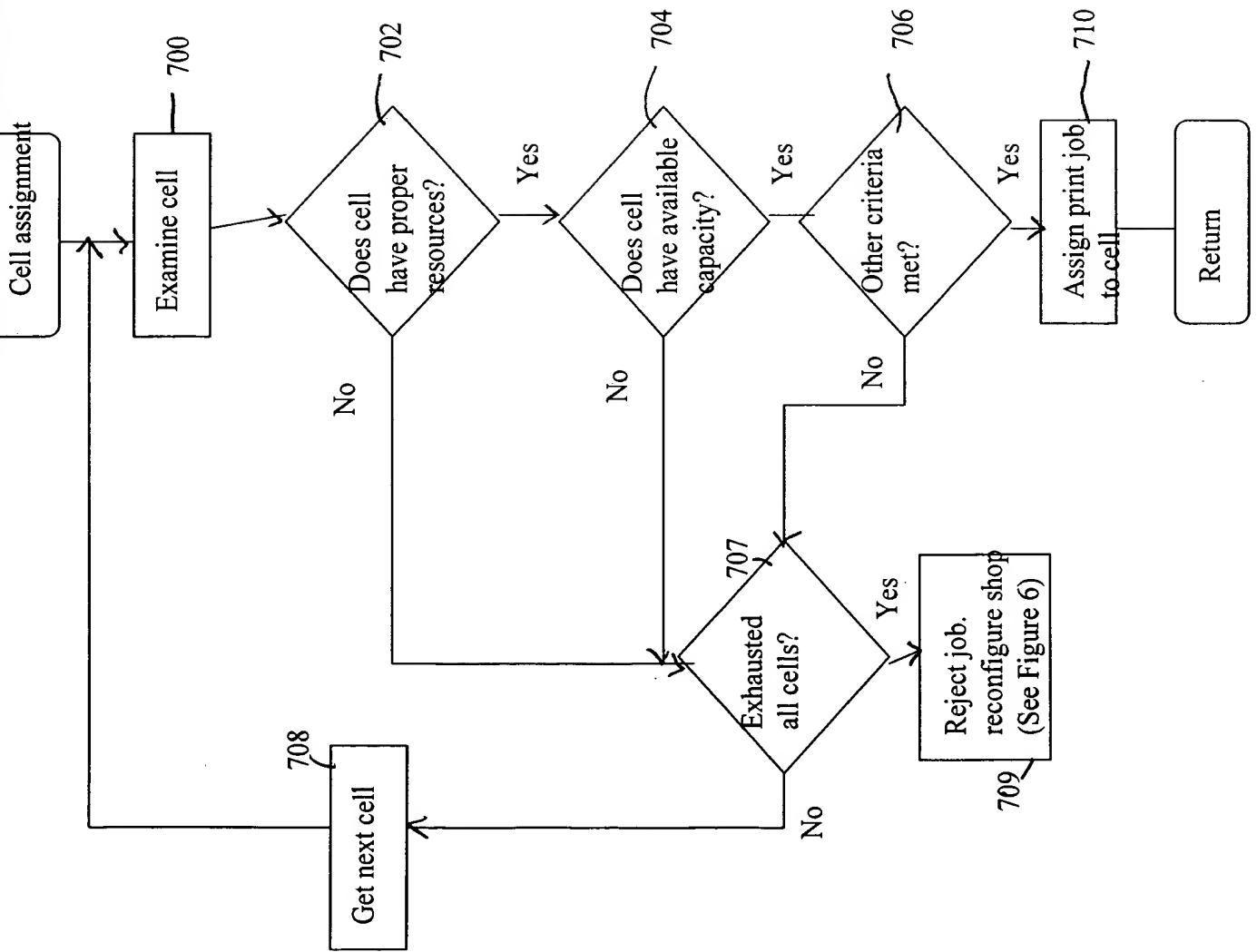


Figure 7.